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## FACSIMILE COVER SHEET

**DATE:** February 28, 2006

**TO:** Examiner Brian K. Talbot **FAX NO.:** 571-273-8300

USPTO GPAU: 1762

**FROM:** Jeffrey S. Abel

Registration No.: 36,079

**RE U.S. App. No.:** 10/816,045, filed April 1, 2004

**Applicant(s):** Venkat Selvamanickam, et al.

**Atty Dkt No.:** 1014-SP127

**Title:** SUPERCONDUCTOR FABRICATION PROCESSES

**NO. OF PAGES (including Cover Sheet):** 4

### MESSAGE:

Attached please find:

- ☒ Transmittal Form (1 pg)
- ☒ Response to Restriction Requirement (2 pgs)

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PTO/SB/21 (09-04)

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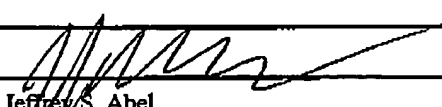
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<b>TRANSMITTAL FORM</b>  <small>(to be used for all correspondence after initial filing)</small>	Application Number	10/816,045	
	Filing Date	04/01/2004	
	First Named Inventor	Venkat Selvamanickam	
	Art Unit	1762	
	Examiner Name	Brian K. Talbot	
Total Number of Pages In This Submission	3	Attorney Docket Number	1014-SP127

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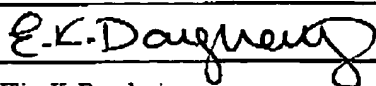
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Remarks  <div style="text-align: center; font-size: 1.2em;">CUSTOMER NO.: 34456</div>		

**SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT**

Firm Name			
Signature			
Printed name	Jeffrey S. Abel		
Date	2/28/06	Reg. No.	36,079

**CERTIFICATE OF TRANSMISSION/MAILING**

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below:

Signature			
Typed or printed name	Elise K. Dougherty	Date	2/28/06

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): Venkat Selvamanickam, et al.

Title: SUPERCONDUCTOR FABRICATION PROCESSES

App. No.: 10/816,045 Filed: 04/01/2004

Examiner: Brian K. Talbot Group Art Unit: 1762

Atty. Dkt. No.: 1014-SP127

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MS Amendment  
Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

**RESPONSE TO RESTRICTION REQUIREMENT**

Dear Sir:


The USPTO has restricted claims 1-38 of this application into Group I (claims 1-33), Group II (claims 34-35), Group III (claims 36-37) and Group IV (claim 38).

Applicants elect the claims of Group I (claims 1-33) and provisionally withdraw the non-elected claims of Groups II-IV. The restriction is respectfully traversed in order to preserve the issue for subsequent petition since the examination of all of the claims is not believed to create an undue burden on the USPTO and that the subject matter among the groups is not independent and distinct as required by statute. Furthermore, different classifications as recited by the USPTO are not independent adequate grounds for restriction since the USPTO has historically examined applications containing multiple sets of claims.

In summary, Applicants elect the claims of Group I for further prosecution and provisionally withdraw the non-elected claims from consideration. Reconsideration and further prosecution on the merits of at least the claims of Group I are respectfully requested.

Respectfully submitted,

Date 2/28/06

  
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